Form A2-3: Compliance Verification Report for Type A Inverter Connected Power Generating Modules

This form should be used by the **Manufacturer** to demonstrate and declare compliance with the requirements of EREC G99/NI. The form can be used in a variety of ways as detailed below:

1. To obtain **Fully Type Tested** status

   The **Manufacturer** can use this form to obtain **Fully Type Tested** status for a **Power Generating Module** by registering this completed form with the Energy Networks Association (ENA) Type Test Verification Report Register.

2. To obtain **Type Tested** status for a product

   This form can be used by the **Manufacturer** to obtain **Type Tested** status for a product which is used in a **Power Generating Module** by registering this form with the relevant parts completed with the Energy Networks Association (ENA) Type Test Verification Report Register.

3. One-off Installation

   This form can be used by the **Manufacturer** or **Installer** to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99/NI. This form shall be submitted to the **DNO** as part of the application.

A combination of (2) and (3) can be used as required, together with Form A2-4 where compliance of the **Interface Protection** is to be demonstrated on site.

Note:

Within this Form A2-3 the term **Power Park Module** will be used but its meaning can be interpreted within Form A2-3 to mean **Power Park Module**, **Generating Unit or Inverter** as appropriate for the context. However, note that compliance shall be demonstrated at the **Power Park Module** level.

If the **Power Generating Module** is **Fully Type Tested** and registered with the Energy Networks Association (ENA) Type Test Verification Report Register, the Installation Document (Form A3-1 or A3-2) should include the **Manufacturer's** reference number (the Product ID), and this form does not need to be submitted.

Where the **Power Generating Module** is not registered with the ENA Type Test Verification Report Register or is not **Fully Type Tested** this form (all or in parts as applicable) needs to be completed and provided to the **DNO**, to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99/NI.

PGM technology		Hybrid Inverter	
Manufacturer name		EcoFlow Inc.	
Address		RM 401, Plant #1, Runheng Industrial Zone, Fuyuanyi Road, Zhancheng Community, Fuhai Street, Bao'an District, Shenzhen City, Guangdong Province, P.R. China	
Tel	+86 18913539557	Web site	https://www.ecoflow.com/uk
E:mail	Boyi.meng@ecoflow.com		
Registered Capacity		EF HD-P1-4K6-S2, EF HD-P1-5K-S2, EF HD-P1-5K75-1S2, EF HD-P1-6K-S2	

There are four options for Testing: (1) **Fully Type Tested**, (2) Partially **Type Tested**, (3) one-off installation, (4) tested on site at time of commissioning. The check box below indicates which tests in this Form have been completed for each of the options. With the exception of **Fully Type Tested PGM**s tests marked with * may be carried out at the time of commissioning (Form A4).
Insert Document reference(s) for **Manufacturers' Information**

Tested option:	**1. Fully Type Tested**	**2. Partially Type Tested**	**3. One-off Man. Info.**	**4. Tested on Site at time of Commissioning**
0. **Fully Type Tested** - all tests detailed below completed and evidence attached to this submission	**P**	**N/A**	**N/A**	**N/A**
1. Operating Range	**N/A**			
2. PQ – Harmonics				
3. PQ – Voltage Fluctuation and Flicker				
4. PQ – DC Injection (**Power Park Module**s only)				
5. **Power Factor** (PF)*				
6. Frequency protection trip and ride through tests*				
7. Voltage protection trip and ride through tests*				
8. Protection – Loss of Mains Test*, Vector Shift and RoCoF Stability Test*				
9. **LFSM-O** Test*				
10. Protection – Reconnection Timer*				
11. Fault Level Contribution				

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12. Self-monitoring Solid State Switch				
13. Wiring functional tests if required by para 15.2.1 (attach relevant schedule of tests)*				
14. Logic Interface (input port)*				

* may be carried out at the time of commissioning (Form A.2-4).

Manufacturer compliance declaration. - I certify that all products supplied by the company with the above **Type Tested Manufacturer**'s reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site **Modification**s are required to ensure that the product meets all the requirements of EREC G99/NI.

Signed	Boyi Meng	On behalf of	EcoFlow Inc.

Note that testing can be done by the **Manufacturer** of an individual component or by an external test house.

Where parts of the testing are carried out by persons or organisations other than the **Manufacturer** then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.

A2-3 Compliance Verification Report – Tests for Type A Inverter Connected Power Generating Modules – test record

1. Operating Range: Tests should be carried with the **Power Generating Module** operating at **Registered Capacity** and connected to a suitable test supply or grid simulation set. The power supplied by the primary source shall be kept stable within ± 5 % of the apparent power value set for the entire duration of each test sequence.

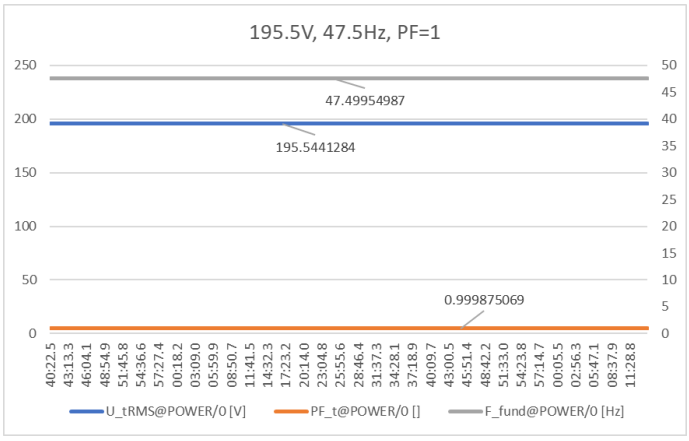
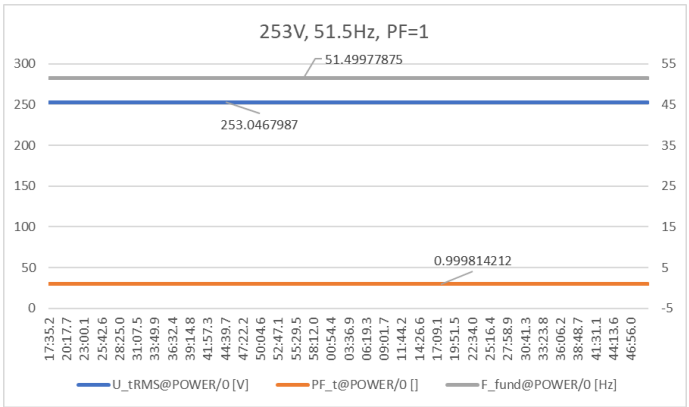
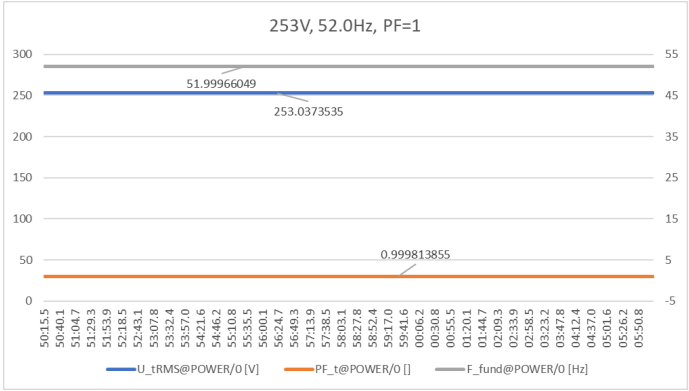
Frequency, voltage and **Active Power** measurements at the output terminals of the **Power Generating Module** shall be recorded every second. The tests will verify that the **Power Generating Module** can operate within the required ranges for the specified period of time.

The **Interface Protection** shall be disabled during the tests.

In case of a PV **Power Park Module** the PV primary source may be replaced by a DC source.

In case of a full converter **Power Park Module** (eg wind) the primary source and the prime mover I**nverter**/rectifier may be replaced by a DC source.

Model: EF HD-P1-6K-S2

Test 1 Voltage = 85% of nominal (195.5 V), Frequency = 47.5 Hz, Power Factor = 1, Period of test 90 minutes	
Test 2 Voltage = 110% of nominal (253 V)., Frequency = 51.5 Hz, Power Factor = 1, Period of test 90 minutes	
Test 3 Voltage = 110% of nominal (253 V), Frequency = 52.0 Hz, Power Factor = 1, Period of test 15 minutes	

2. Power Quality – Harmonics:

For **Power Generating Module**s of **Registered Capacity** of less than 75 A per phase (ie 50 kW) the test requirements are specified in Annex A.7.1.5**.** These tests should be carried out as specified in BS EN 61000-3-12. The results need to comply with the limits of Table 2 of BS EN 61000-3-12 for single phase equipment and Table 3 of BS EN 610000-3-12 for three phase equipment.
Power Generating Modules with emissions close to the limits laid down in BS EN 61000-3-12 may require the installation of a transformer between 2 and 4 times the rating of the **Power Generating Module** in order to accept the connection to a **Distribution Network.**

For **Power Generating Module**s of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation shall be designed in accordance with EREC G5.

Power Generating Module tested to BS EN 61000-3-12

Model: EF HD-P1-6K-S2

Power Generating Module rating per phase (rpp)			**6**	kVA	Harmonic % = Measured Value (A) x 23/rating per phase (kVA)	
Harmonic	At 45-55% of **Registered Capacity**		100% of **Registered Capacity**		Limit in BS EN 61000-3-12	
	Measured Value (A)	%	Measured Value (A)	%	1 phase	3 phase
2	0.0125	0.096	0.0320	0.124	8%	8%
3	0.1509	1.162	0.0983	0.381	21.6%	Not stated
4	0.0034	0.026	0.0094	0.037	4%	4%
5	0.1129	0.870	0.0816	0.317	10.7%	10.7%
6	0.0050	0.039	0.0038	0.015	2.67%	2.67%
7	0.0356	0.274	0.0793	0.308	7.2%	7.2%
8	0.0021	0.016	0.0089	0.034	2%	2%
9	0.1064	0.820	0.1278	0.496	3.8%	Not stated
10	0.0029	0.022	0.0041	0.016	1.6%	1.6%
11	0.1202	0.926	0.1540	0.597	3.1%	3.1%
12	0.0042	0.032	0.0039	0.015	1.33%	1.33%
13	0.0923	0.711	0.1004	0.389	2%	2%
THD[20]		2.549		1.497	23%	13%
PWHD[21]		6.884		4.990	23%	22%

[20] THD = Total Harmonic Distortion

[21] PWHD = Partial Weighted Harmonic Distortion

Model: EF HD-P1-4K6-S2						
Power Generating Module rating per phase (rpp)			**4.6**	kVA	Harmonic % = Measured Value (A) x 23/rating per phase (kVA)	
Harmonic	At 45-55% of **Registered Capacity**		100% of **Registered Capacity**		Limit in BS EN 61000-3-12	
	Measured Value (A)	%	Measured Value (A)	%	1 phase	3 phase
2	0.0124	0.126	0.0369	0.185	8%	8%
3	0.1465	1.483	0.1550	0.778	21.6%	Not stated
4	0.0069	0.070	0.0086	0.043	4%	4%
5	0.1031	1.044	0.1041	0.522	10.7%	10.7%
6	0.0099	0.100	0.0159	0.080	2.67%	2.67%
7	0.0303	0.306	0.0371	0.186	7.2%	7.2%
8	0.0056	0.056	0.0049	0.025	2%	2%
9	0.1075	1.089	0.1418	0.711	3.8%	Not stated
10	0.0040	0.040	0.0073	0.037	1.6%	1.6%
11	0.0883	0.893	0.1174	0.589	3.1%	3.1%
12	0.0045	0.045	0.0024	0.012	1.33%	1.33%
13	0.0635	0.643	0.1338	0.671	2%	2%
THD[20]		2.908		2.030	23%	13%
PWHD[21]		7.673		6.140	23%	22%

[22] THD = Total Harmonic Distortion

[23] PWHD = Partial Weighted Harmonic Distortion

3. Power Quality – Voltage fluctuations and Flicker:

For **Power Generating Module**s of **Registered Capacity** of less than 75 A per phase (ie 50 kW) these tests should be undertaken in accordance with Annex A.7.1.4.3. Results should be normalised to a standard source impedance, or if this results in figures above the limits set in BS EN 61000-3-11 to a suitable Maximum Impedance.

For **Power Generating Module**s of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation shall be designed in accordance with EREC P28.

Model: EF HD-P1-6K-S2

	Starting			Stopping			Running	
	d max	d c	d(t)	d max	d c	d(t)	P st	P lt 2 hours
Measured Values at test impedance	0.504%	0.003%	0	0.499%	0.010%	0	0.028	0.028
Normalised to standard impedance	0.504%	0.003%	0	0.499%	0.010%	0	0.028	0.028
Normalised to required maximum impedance	0.504%	0.003%	0	0.499%	0.010%	0	0.028	0.028
Limits set under BS EN 61000-3-11	4%	3.3%	3.3%	4%	3.3%	3.3%	1.0	0.65

Test Impedance	R	0.4	Ω	Xl	0.25	Ω
Standard Impedance	R	0.24 * 0.4 ^	Ω	Xl	0.15 * 0.25 ^	Ω
Maximum Impedance	R	0.4	Ω	Xl	0.25	Ω

* Applies to three phase and split single phase **Power Generating Module**s.

^ Applies to single phase **Power Generating Module** and **Power Generating Module**s using two phases on a three phase system

For voltage change and flicker measurements the following formula is to be used to convert the measured values to the normalised values where the **Power Factor** of the generation output is 0.98 or above.

Normalised value = Measured value x reference source resistance/measured source resistance at test point

Single phase units reference source resistance is 0.4 Ω

Two phase units in a three phase system reference source resistance is 0.4 Ω

Two phase units in a split phase system reference source resistance is 0.24 Ω

Three phase units reference source resistance is 0.24 Ω

Where the **Power Factor** of the output is under 0.98 then the Xl to R ratio of the test impedance should be close to that of the Standard Impedance.

The stopping test should be a trip from full load operation.

The duration of these tests need to comply with the particular requirements set out in the testing notes for the technology under test. Dates and location of the test need to be noted below

Test start date	2024-12-25	Test end date	2024-12-25
Test location	EcoFlow Inc. RM 401, Plant #1, Runheng Industrial Zone, Fuyuanyi Road, Zhancheng Community, Fuhai Street, Bao'an District, Shenzhen City, Guangdong Province, P.R. China		

4. Power quality – DC injection: The tests should be carried out on a single **Generating Unit**. Tests are to be carried out at three defined power levels ±5%. At 230 V a 50 kW three phase **Inverter** has a current output of 217 A so DC limit is 543 mA. These tests should be undertaken in accordance with Annex A.7.1.4.4.

Model: EF HD-P1-6K-S2			
Test power level	10%	55%	100%
Recorded value in Amps	0.0216 A	0.051 A	0.062 A
as % of rated AC current	0.08%	0.19%	0.23%
Limit	0.25%	0.25%	0.25%
Model: EF HD-P1-4K6-S2			
Test power level	10%	55%	100%
Recorded value in Amps	0.0217 A	0.0467 A	0.0485 A
as % of rated AC current	0.25%	0.23%	0.24%
Limit	0.25%	0.25%	0.25%

5. Power Factor: The tests should be carried out on a single **Power Generating Module**. Tests are to be carried out at three voltage levels and at **Registered Capacity**. Voltage to be maintained within ±1.5% of the stated level during the test. These tests should be undertaken in accordance with Annex A.7.1.4.2.

Model: EF HD-P1-6K-S2			
Voltage	0.94 pu (216.2 V)	1 pu (230 V)	1.1 pu (253 V)
Measured value	0.999	0.999	0.999
Power Factor Limit – leading	>0.95	>0.95	>0.95
Power Factor Limit – lagging	>0.98	>0.98	>0.98

Model: EF HD-P1-4K6-S2			
Voltage	0.94 pu (216.2 V)	1 pu (230 V)	1.1 pu (253 V)
Measured value	0.999	0.999	0.999
Power Factor Limit – leading	>0.95	>0.95	>0.95
Power Factor Limit – lagging	>0.98	>0.98	>0.98

6. Protection – Frequency tests: These tests should be carried out in accordance with the Annex A.7.1.2.3.

Model: EF HD-P1-6K-S2

Function	Setting		Trip test		"No trip tests"	
	Frequency	Time delay	Frequency	Time delay	Frequency /time	Confirm no trip
U/F	48.0 Hz	0.5 s	47.99Hz	0.54s	48.2 Hz 25 s	No trip
					47.8 Hz 0.45 s	No trip
O/F	52 Hz	1.0 s	52.01Hz	1.05s	51.8 Hz 120 s	No trip
					52.2 Hz 0.98 s	No trip

Note. For frequency trip tests the frequency required to trip is the setting ± 0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The "No trip tests" need to be carried out at the setting ± 0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

7. Protection – Voltage tests: These tests should be carried out in accordance with Annex A.7.1.2.2.

Model: EF HD-P1-6K-S2

Function	Setting		Trip test		"No trip tests"	
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip
U/V stage1	195.5 V	3.0 s	193.6V	3.07s	199.5 V 5 s	No trip
U/V stage2	138.0 V	2 s	136.6V	2.04s	142.0 V 2.5 s	No trip
					134 V 1.98 s	No trip
O/V	253 V	0.5 s	251.2V	0.52s	249 V 5.0 s	No trip
					257 V 0.45 s	No trip

Note for Voltage tests the Voltage required to trip is the setting ±3.45 V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting ±4 V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.						
8.Protection – Loss of Mains test: These tests should be carried out in accordance with BS EN 62116. Annex A.7.1.2.4.						
Model: EF HD-P1-6K-S2						
The following sub set of tests should be recorded in the following table.						
Test Power and imbalance	33% -5% Q Test 22	66% -5% Q Test 12	100% -5% P Test 5	33% +5% Q Test 31	66% +5% Q Test 21	100% +5% P Test 10
Trip time. Limit is 0.5s	0.439s	0.436s	0.409s	0.451s	0.430s	0.432s

Loss of Mains Protection, Vector Shift Stability test. This test should be carried out in accordance with Annex A.7.1.2.6.			
	Start Frequency	Change	Confirm no trip
Positive Vector Shift	49.5 Hz	+50 degrees	No trip
Negative Vector Shift	50.5 Hz	- 50 degrees	No trip

Loss of Mains Protection, RoCoF Stability test: This test should be carried out in accordance with Annex A.7.1.2.6.			
Ramp range	Test frequency ramp:	Test Duration	Confirm no trip
49.0 Hz to 51.0 Hz	+0.95 Hzs^{-1}	2.1 s	No trip
51.0 Hz to 49.0 Hz	-0.95 Hzs^{-1}	2.1 s	No trip

9. Limited Frequency Sensitive Mode – Over frequency test: The test should be carried out using the specific threshold frequency of 50.2 Hz and **Droop** of 4%. This test should be carried out in accordance with Annex A.7.1.3.	
Active Power response to rising frequency/time plots are attached if frequency injection tests are undertaken in accordance with Annex A.7.2.4.	**Y**
Alternatively, simulation results should be noted below:	
Model: EF HD-P1-6K-S2	

Test sequence at **Registered Capacity** >80%	Measured **Active Power** Output	Frequency	Primary Power Source	**Active Power** Gradient
Step a) 50.00Hz ±0.01Hz	5949.4W	50.00Hz	Photovoltaic array simulator	--
Step b) 50.25Hz ±0.05Hz	5834.7W	50.25Hz		--
Step c) 50.70Hz ±0.10Hz	4468.6W	50.70Hz		--
Step d) 51.15Hz ±0.05Hz	3097.0W	51.15Hz		--
Step e) 50.70Hz ±0.10Hz	4519.4W	50.70Hz		--
Step f) 50.25Hz ±0.05Hz	5853.5W	50.25Hz		--
Step g) 50.00Hz ±0.01Hz	5986.8W	50.00Hz		--
Test sequence at **Registered Capacity** 40% - 60%	Measured **Active Power** Output	Frequency	Primary Power Source	**Active Power** Gradient
Step a) 50.00Hz ±0.01Hz	2996.0W	50.00Hz	Photovoltaic array simulator	--
Step b) 50.25Hz ±0.05Hz	2848.2W	50.25Hz		--
Step c) 50.70Hz ±0.10Hz	1471.7W	50.70Hz		--
Step d) 51.15Hz ±0.05Hz	110.3W	51.15Hz		--
Step e) 50.70Hz ±0.10Hz	1500.4W	50.70Hz		--
Step f) 50.25Hz ±0.05Hz	2886.8W	50.25Hz		--
Step g) 50.00Hz ±0.01Hz	3004.2W	50.00Hz		--

10. Protection – Re-connection timer.					
Test should prove that the reconnection sequence starts after a minimum delay of 60 s for restoration of voltage and frequency to within the stage 1 settings of Table 10.1.					
Model: EF HD-P1-6K-S2					
Time delay setting	Measured delay	Checks on no reconnection when voltage or frequency is brought to just outside stage 1 limits of Table 10.1.			
60s	130s	At 257.0 V	At 191.5 V	At 47.9 Hz	At 52.1 Hz
Confirmation that the **Power Generating Module** does not re-connect.		No re-connet	No re-connet	No re-connet	No re-connet

11. Fault level contribution: These tests shall be carried out in accordance with EREC G99/NI Annex A.7.1.5.		
For **Inverter** output		
Model: EF HD-P1-6K-S2		
Time after fault	Volts	Amps
20 ms	100	40
100 ms	60	0
250 ms	30	0
500 ms	0	0
Time to trip	0.0258	In seconds

12. Self-Monitoring solid state switching: No specified test requirements. Refer to Annex A.7.1.7.	
It has been verified that in the event of the solid state switching device failing to disconnect the **Power Park Module**, the voltage on the output side of the switching device is reduced to a value below 50 volts within 0.5 s.	NA
13. Wiring functional tests: If required by para 15.2.1.	
Confirm that the relevant test schedule is attached (tests to be undertaken at time of commissioning)	NA
14. Logic interface (input port).	
Confirm that an input port is provided and can be used to shut down the module.	Yes
Additional comments.	
To open or short pin14 and pin16 of logic interface port to control the inverter to normal or shutdown active power of output. A logic interface is provided that can be operated by an external switch or contactor. Users can install by themselves. Users install the switch connected to pin14 and pin16 of interface port and just need control the switch signal causing the switch to open or short. When the switch is open, the inverter will operate normally. When the switch is closed, the inverter will cease to export active power immediately. The signal from the inverter that is being switched is DC (maximum value 3.3V).	